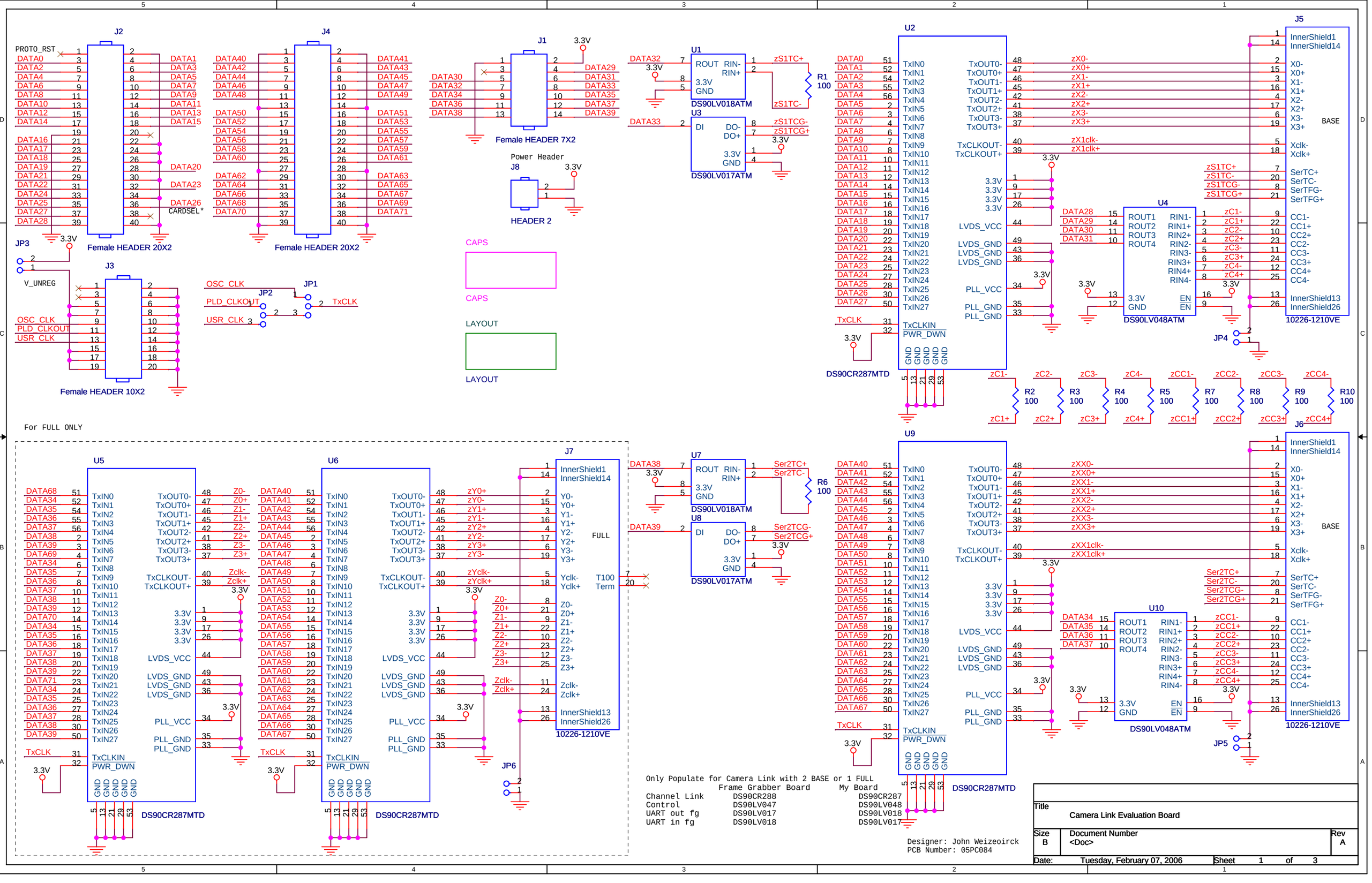
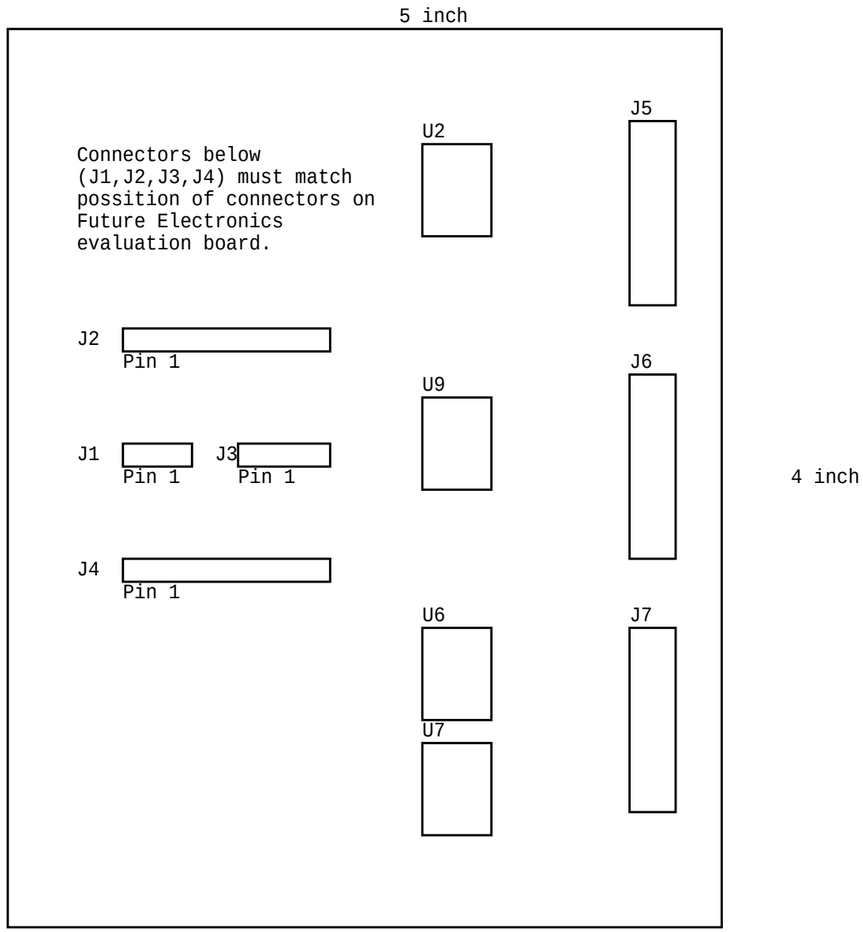

J2
Female HEADER 20X2
J4
Female HEADER 20X2
J1
Female HEADER 7X2
Power Header
J8
HEADER 2
JP3
V_UNREG
J3
Female HEADER 10X2
OSC_CLK
PLD_CLKOUT
USR_CLK
JP2
JP1
TxCLK
CAPS
CAPS
LAYOUT
LAYOUT
U1
DS90LV018ATM
U3
DS90LV017ATM
R1
100
U2
DS90CR287MTD
U4
DS90LV048ATM
J5
10226-1210VE
JP4
R2
R3
R4
R5
R7
R8
R9
R10
For FULL ONLY
U5
DS90CR287MTD
U6
DS90CR287MTD
J7
10226-1210VE
JP6
U7
DS90LV018ATM
U8
DS90LV017ATM
R6
100
U9
DS90CR287MTD
U10
DS90LV048ATM
J6
10226-1210VE
JP5
Only Populate for Camera Link with 2 BASE or 1 FULL
Frame Grabber Board
My Board
Channel Link DS90CR288 DS90CR287
Control DS90LV047 DS90LV048
UART out fg DS90LV017 DS90LV018
UART in fg DS90LV018 DS90LV017
Designer: John Weizeoirck
PCB Number: 05PC084
Title
Camera Link Evaluation Board
Size B
Document Number
<Doc>
Rev A
Date: Tuesday, February 07, 2006
Sheet 1 of 3



Title	Camera Link - PCB Layout	
Size A	Document Number <Doc>	Rev <RevCode>
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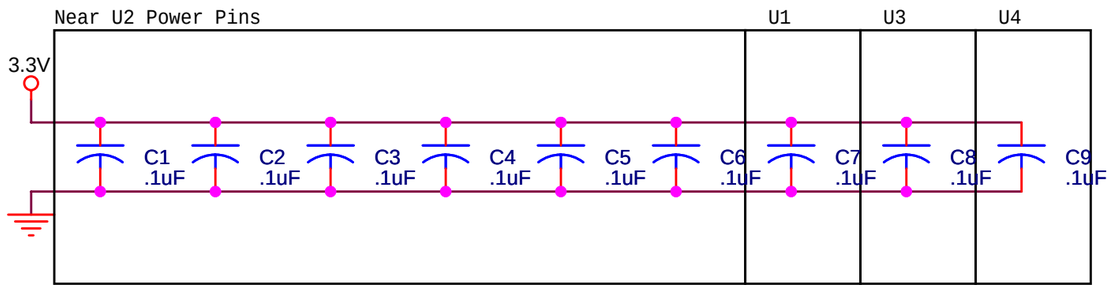
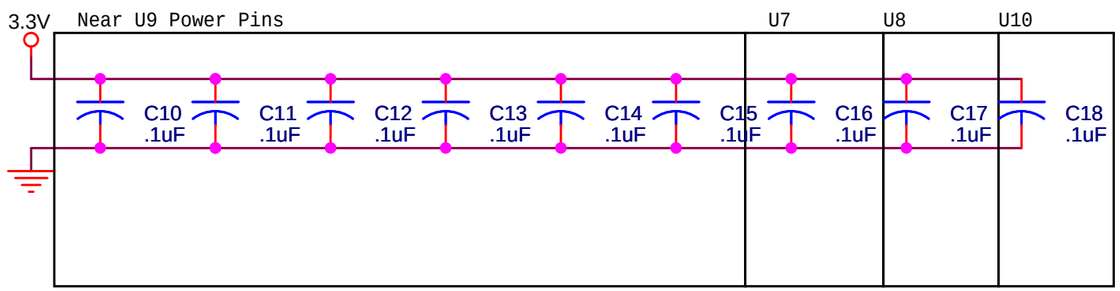
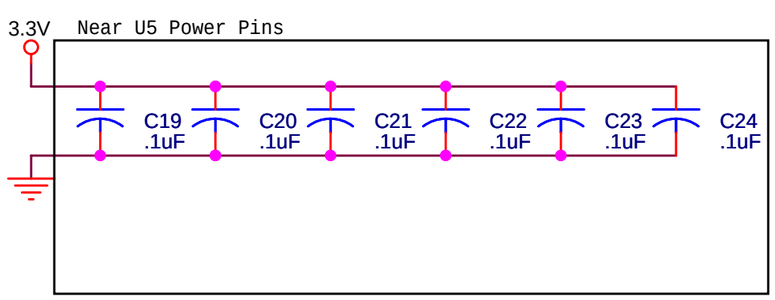
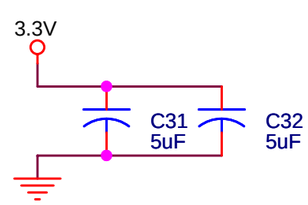
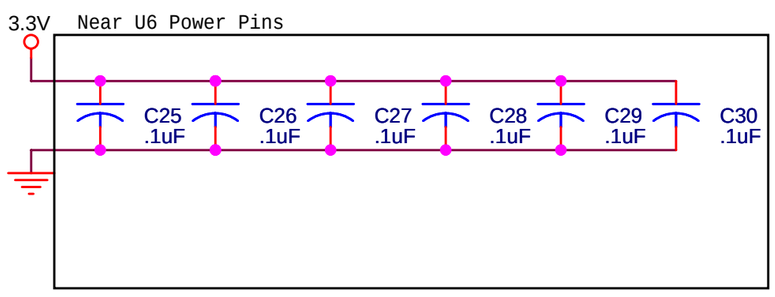
1 - Caps C1 -> C30 use a 1206 package
2 - Caps C31 and C32 use a 2012 package
3 - All traces that start with z are 100 ohms impeadence matched LVDS signals.
See the Channellink_design_guide.doc for PCB Recommendations











Title	Camera Link - Power Supply Capacitors	
Size A	Document Number <Doc>	Rev A
Date: Tuesday, February 07, 2006	Sheet 2 of 3	